
Datalight FlashFX Tera User Manual

General Description and Name

This BBM is worked for Bulk Programming NAND Flash for Datalight FlashFX Tera. There will be a BB table generate by this BBM and ECC fields within spare area updated according to Broadcom algorithm.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.

Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:

<http://www.dataio.com/support/dsr.asp>

Bad Block Handling Type = “Datalight FlashFX Tera”

Spare area : Please refer to “Description of common NAND special features.pdf”. Normally set as “ECC” for this BBM.[Default ‘Disabled’]

ECC store from : Which byte within spare area to start storing the ECC result, 0 based, hex value that fill 20 means 32 if decimal base. Normally set as “ 20” for 2048 byte page device and “8” for 512 byte page device.[Default ‘20’]

BB: max allowed : Please refer to “Description of common NAND special features.pdf”. Normally keep as default for this BBM.[Default ‘FFFFFFFF’ means keep BBM default]

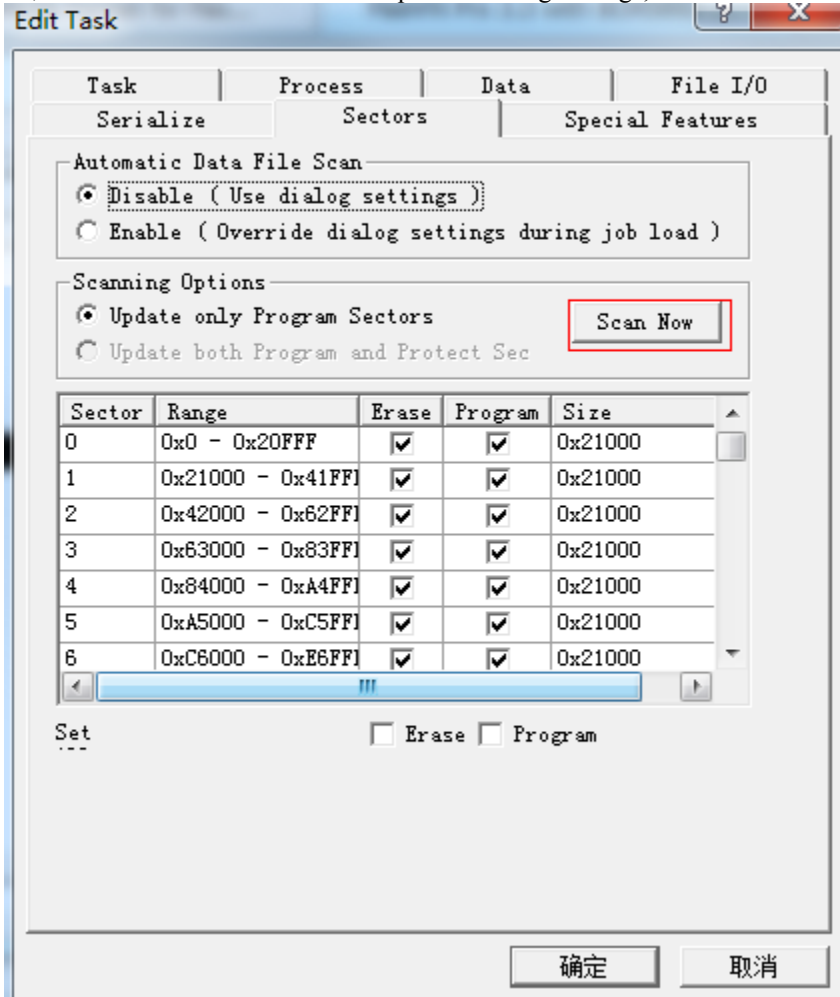
Only Cal ECC for User Data : Please refer to “Description of common NAND special features.pdf”. Normally set as “NO” for this BBM.[Default ‘YES’]

Special Notes

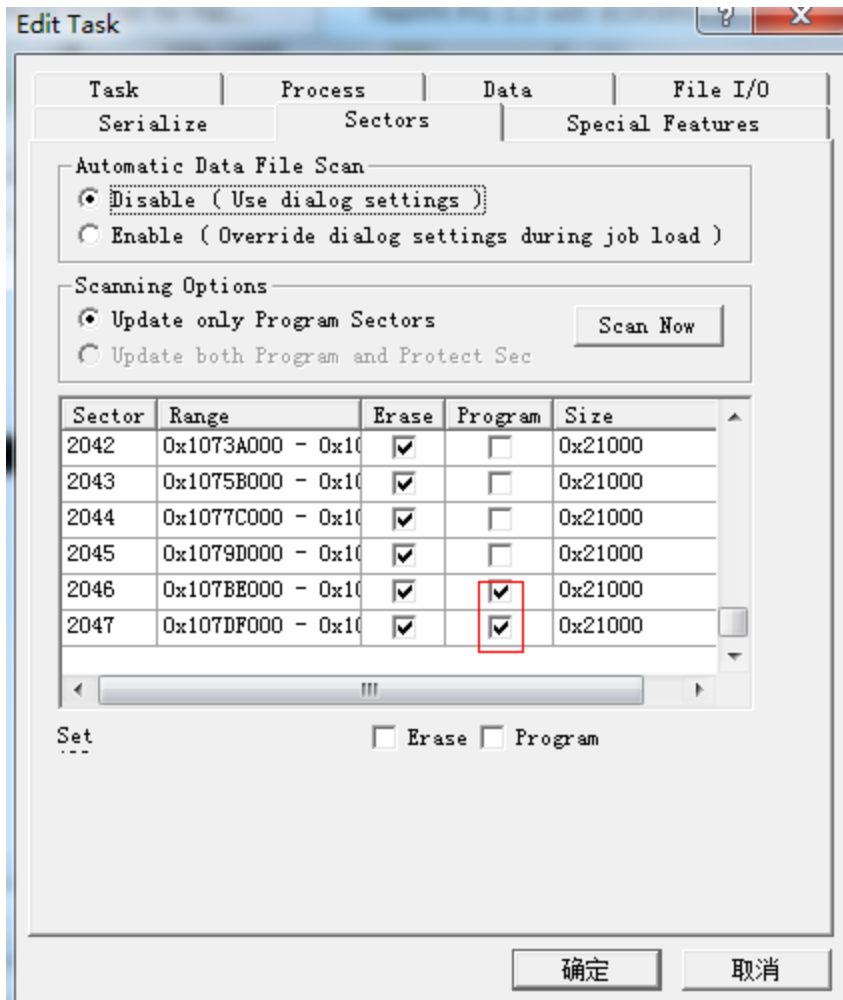
Even only Spare area as “ECC” is suggested as customer’s data file doesn’t include any spare area data, this BBM also support this special feature as “Enabled” and “Update ECC Field”, please refer to “Description of common NAND special features.pdf” for preparing your data file accordingly if needed.

As Only Cal ECC for User Data should be set as “NO”, please follow below suggested steps to save the programmer operation time by skipping all the empty blocks within data file or data file doesn’t cover.

- 1st, set Only Cal ECC for User Data as “YES” and Spare area as “Disabled”;
- 2nd, set all other settings of this task as expected;
- 3rd, use “Scan Now” button to update the Program tags;



- 4th, Click the very last two sectors to select it;



5th, recover Only Cal ECC for User Data as “NO” and Spare area as expected;
 6th, load the task as normal.

Revision History

V1.0 Date: 2012-May-16

Create this spec, only 512 byte per page and 2048 byte per page currently supported.

V1.1 Date: 2014-Oct-17

fix a bug when there is a bad block at the last two blocks.

Appendix

You can get the file “Description of common NAND special features.pdf” from <http://ftp.dataio.com/FCNotes/BBM/>