# **Unistore v1.0 User Manual**

# **General Description and Name**

Unistore v1.8 Bad Block Method. This method divides the device into two sections, a reservoir and the main array. The reserved area has a few different partitions while the main array contains the customer's data file.

### **Relevant User Options**

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.

Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:

http://www.dataio.com/support/dsr.asp

<u>Bad Block Handling Type</u> = "Unistore v1.0"

<u>Spare area</u>: Please refer to "Description of common NAND special features.pdf". *Always set as* "*ECC*" *for this BBM*.[Default 'Disabled']

RBA area: Start block = "4020"

RBA area: Number of blocks = "76"

UBA area: Start block = "0"

<u>Required good block area: Start block</u> = "0" Please refer to "Description of common NAND special features.pdf".

<u>Required good block area: Number of blocks</u> = "0" Please refer to "Description of common NAND special features.pdf".

#### **Special Notes**

The spare area in this scheme uses a special ECC method provided by Samsung.

The bad block marks are always located in the spare area (Byte 517 for x8 devices).

Customer provided ECC calculation is used in TLWin during data file generation and during the programming of the map table pages by the FlashPAK. The ECC data is located at byte 525 of the block and is three bytes in size.

The data file doesn't have to be arranged in any special way for this scheme. The binary that should be placed into the device is all that is needed.

For extensive details, please refer to UniStore Pre-programming Guide.pdf

# **Revision History**

V1.0 June 11, 2009 Create this spec.

# **Appendix**

You can get the file "Description of common NAND special features.pdf" from http://ftp.dataio.com/FCNotes/BBM/

